

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Olivier Charles Schuepbach

Docket No.: TI-33782

Serial No.: TBD

Examiner: TBD

Filed: Herewith

Art Unit: TBD

For: RSSE Optimization Using Hardware Acceleration

Conf. No.: TBD

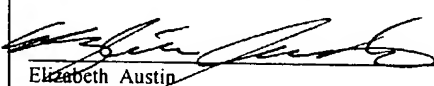


INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner For Patents
Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Assistant Commissioner for Patents, Alexandria, VA 22313-1450.


Elizabeth Austin

 7/31/2003
Date

Dear Sir:

In response to the Search Report received from the European Patent Office (a copy of which is enclosed), Applicant wishes to bring to the attention of the U.S. Patents and Trademarks Office the information noted on the enclosed PTO-1449. Copies of the cited references are enclosed.

If required, charge the \$240 fee to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

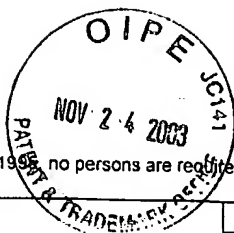
Respectfully submitted,



Ronald O. Neerings
Attorney for Applicants
Reg. No. 34,227

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972) 917-5299

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0651-0031
Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Substitute for Form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Complete If Known	
Application Number		TBD			
Filing Date		Herewith			
First Named Inventor		Olivier Charles Schuepbach			
Group Art Unit		TBD			
Examiner Name		TBD			
Attorney Docket No.		TI-33782			

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					
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	AJ					
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	AM					
	AN					
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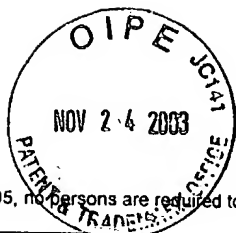
FOREIGN PATENT DOCUMENTS								
Exam. Initials*	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Office ³	Number ⁴	Kind Code ² (if known)				
	BA		EP 0 786 872 A2		Lucent Technologies Inc.	30/07/1997		
	BB		EP 1 058 392 A1		Motorola, Inc.	06/12/2000		
	BC		WO 01/03308 A1		Fraun-Hofer-Gesellschaft Zur Forderung Der Angewandten Forschung E.V.	11/01/2001		
	BD		WO 01/60002 A1		Ericsson Inc.	17/01/2001		
	BE		WO 01 78239 A2		Qualcomm Inc.	18/10/2001		
	BF		WO 02/39689 A2		Telefonaktiebolaget LM Ericsson	16/05/2001		
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²See attached Kinds of U.S. Patent Documents. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

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				First Named Inventor	Olivier Charles Schuepbach
				Group Art Unit	TBD
				Examiner Name	TBD
				Attorney Docket No.	TI-33782
Sheet	2	of	2		

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

Exam. Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	CA	"Error Probability of Reduced State Sequence Estimation for Trellis-Coded Modulation on Intersymbol Interference Channels", Wern Ho Sheen, et al., IEEE Global Telecommunications Conference, 2-5 December 1991, pp. 1159-1164, XP010042585, New York, US	
	CB	"Reduced-Complexity Viterbi Detector Architectures for Partial Response Signalling", Gerhard Fettweis, et al., IEEE Global Telecommunications Conference, 14-16 November 1995, pp. 559-563, XP000621547, New York, US	
	CC	"Digital Communications", J. G. Proakis, McGraw-Hill, 1995, pp. 249-254, 468-470, 621-627, 649-654, 698-702, 729-732, and 797-806, XP002202678, New York, US, ISBN: 0-07-113814-5	
	CD		
	CE		
	CF		
	CG		
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	CR		

Examiner Signature		Date Considered	
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